

Class	ISSUE CLASS
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# U.S. UTILITY Patent Application

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APPLICATION NO. 09/534043	CONT/PRIOR F	CLASS 257	SUBCLASS 712	ART UNIT 2815	EXAMINER Cru
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APPLICANTS  
Toshiba America Electronic  
Corporation  
Toshiba America Electronic  
Corporation

TITLE  
Semiconductor device and method of manufacturing the same

PTO-2040  
1999

## ISSUING CLASSIFICATION

CLASS	SUBCLASS	CROSS REFERENCES			
		CLASS	SUBCLASS	ART UNIT	EXAMINER
257	712				

Continued on Issue 842 inside this patent

TERMINAL DISCLOSURE	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drawg.	Figs. Drawg.	Print Fig.	Total Claims	Print Claims for C.G.
<input type="checkbox"/> Section of this patent is hereby disclaimed. (date) _____ (Assistant Examiner) _____ (Date) _____				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> Section of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ (Primary Examiner) _____ (Date) _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. (Legal Instruments Examiner) _____ (Date) _____				Amount Due	Date Paid
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